

IN THE SPECIFICATION:

Please amend the paragraph on page 6, lines 8 to 17 as follows:

Hereinafter, referring to the accompanying drawings, a description as to a data processor (or information processing system) and the mode of certain aspects of the preferred embodiments of the system will be made. The circuit elements constituting each block of the embodiment preferably are formed on a semiconductor substrate such as single crystal silicon by means of a technique of semiconductor integrated circuit such as well-known CMOS (complementary MOS transistor) and bipolar transistor or the like, though the elements are not particularly limited thereto. Also, the invention preferably may be realized as an IP intellectual property or design data) module (either soft IP or hard IP) as all or a portion of the design data for forming a semiconductor integrated circuit. The IP module preferably is stored in an information-recording medium, and preferably may be subjected to a trade and/or transfer via a telecommunication line, data network or the like.